L	Hits	Search Text	DB	Time stamp
Number				
1	721	(bipolar adj2 transistor) with (control	USPAT;	2004/09/17
		adj2 circuit)	EPO; JPO;	17:58
			DERWENT	
2	0	input adj2 terminial and barrier and	USPAT;	2004/09/17
		diode and anode and cathode and	EPO; JPO;	17:59
		p"-channel" and MOSFET	DERWENT	
3	0	input adj2 terminial and barrier and	USPAT;	2004/09/17
		diode and anode and cathode and	EPO; JPO;	18:00
		"p-channel" and MOSFET	DERWENT	
4	0	input adj2 terminial and barrier and	USPAT;	2004/09/17
		diode	EPO; JPO;	18:00
			DERWENT	
5	1665	input adj2 terminal and barrier and diode	USPAT;	2004/09/17
			EPO; JPO;	18:00
			DERWENT	
6	1005	anode and cathode and "p-channel" and	USPAT;	2004/09/17
		MOSFET	EPO; JPO;	18:00
			DERWENT	-
7	1	((bipolar adj2 transistor) with (control	USPAT;	2004/09/17
		adj2 circuit) ) and (input adj2 terminal	EPO; JPO;	18:04
		and barrier and diode ) and (anode and	DERWENT	
		cathode and "p-channel" and MOSFET)		
8	1	(((bipolar adj2 transistor) with (control	USPAT;	2004/09/17
		adj2 circuit) ) and (input adj2 terminal	EPO; JPO;	18:04
		and barrier and diode ) and (anode and	DERWENT	
		cathode and "p-channel" and MOSFET)) and		
		resistor		